



**PATENT APPLICATION**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Nobuaki HASHIMOTO

Group Art Unit: 2815

Application No.: 10/788,492

Examiner: N. D. RICHARDS

Filed: March 1, 2004

Docket No.: 118876

For: ELECTRONIC DEVICE AND METHOD OF MANUFACTURING THE SAME,  
CIRCUIT BOARD, AND ELECTRONIC INSTRUMENT

**STATEMENT OF SUBSTANCE OF INTERVIEW**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicant's representative, Dinnatia Doster-Greene, conducted an interview regarding the above-referenced matter with Examiner Richards at the United States Patent and Trademark Office on July 25, 2006. Applicant thanks Examiner Richards for the courtesies extended to the representative.

In the interview, Applicant's representative discussed the Amendment filed July 7, 2006 and explained how the claimed invention is patentably distinct over the primary applied reference, Japanese Patent No. JP 01-164044 (hereinafter "JP044"). Examiner Richards agreed that JP004 does not teach, disclose or suggest a method or apparatus wherein an interconnect covers all the lateral surfaces of a metal layer.

None of the additional applied references of U.S. Patent No. 6,445,069 (Ling) and U.S. patent No. 6,625,032 (Ito) cure the deficiencies of JP004.

Examiner Richards further indicated that he would request a translation of JP044 and that a new search of prior art would be required to determine if the claims, as amended, would be allowable or subject to another Office Action.

Respectfully submitted,



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Dinnatia J. Doster-Greene  
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JAO:DJG/smo

Date: July 28, 2006

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